

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,956,440 B2
APPLICATION NO. : 10/603383
DATED : October 18, 2005
INVENTOR(S) : Yun Y. Choi

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

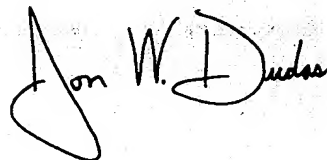
In the Abstract:

At the cover page, (57) Column 2, please replace the abstract to read as follows:

--A PLL can operate linearly at various frequencies by overlapping several VCOs and using a control circuit for controlling the several VCOs to select a VCO at a desired frequency. Accordingly, the PLL can automatically pre-compensate the frequency by using a control signal used in the PLL. As a result, a separate frequency compensation signal is not required. The PLL can be configured on a single chip when the VCO is contained within the PLL.--

Signed and Sealed this

Twenty-third Day of January, 2007

A handwritten signature in black ink, reading "Jon W. Dudas". The signature is stylized with a large, looped "J" and a cursive "Dudas".

JON W. DUDAS
Director of the United States Patent and Trademark Office